



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)		
Robert M. Zeidman)	Examiner:	Ben C. Wang
Serial No.: 10/688,573)	Art Unit:	2196
Filed: October 20, 2003)		
For: Software Tool for Synthesizing a Real-Time Operating System)))		·
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Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Transmittal for an Amendment and Response to Office Action

Dear Sir:

In response to the Office Action mailed November 21, 2006, please find enclosed:

- (1) An Amendment and Response to Office Action;
- (2) Fee Transmittal, in duplicate, and
- (3) Check #_176 in the amount of \$25.00 for the required small entity fees, and



Please associate this patent application with Customer No. 48809.

	Respectfully submitted,	
Date: January 26, 2007	fear	
	Jim H. Salter	
	Registration Number: 35,668	
	Customer No. 48809	
•	105 Thoreau Lane	
	Folsom, CA 95630	
	408-406-4855	

FIRST CLASS MAIL CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450

on January 26, 2007 Date of Deposit	
Jim H. Salter Name of Person Mailing Correspondence	
Thanking correspondence	1/26/07
Signature	. Date



FEE TRANSMITTAL FOR FY 2005

E)	(FY 200	5 Begins 1	10/01/2004. Fee changes made on	11/22/04 and 12/08/04 are included	l.)
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First Named In	ventor <u>R</u>	lobert M.	Zeidman		
Examiner Nam	eB	en C. Wa	ang		
Art Unit	2	196			
Attorney Dock	et No. Z	<u>'eid-01</u>			
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FEE CALCUL	ATION				
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FEE CALCULATION (continued) 3. ADDITIONAL FEES Large Entity Small Entity Fee Fee Fee Fee Code (\$) Code **Fee Description** (\$) Fee Paid 1051 130 2051 65 Surcharge - late filing fee or oath 1052 50 2052 25 Surcharge - late provisional filing fee or cover sheet 1053 130 1053 130 Non-English specification For filing a request for ex parte reexamination 1812 2.520 1812 2,520 1813 8.800 1813 8,800 Request for interparties reexamination 1804 Requesting publication of SIR prior to Examiner action 920* 1804 920* 1,840* 1805 1805 1.840* Requesting publication of SIR after Examiner action 1251 120 2251 60 Extension for reply within first month 1252 450 2252 225 Extension for reply within second month 1253 1.020 2253 510 Extension for reply within third month 1254 1.590 2254 795 Extension for reply within fourth month 1255 2,160 2255 1,080 Extension for reply within fifth month 1401 500 2401 250 **Notice of Appeal** 1402 500 2402 250 Filing a brief in support of an appeal 1403 1,000 2403 500 Request for oral hearing 1451 Petition to institute a public use proceeding 1,510 1451 1,510 1452 500 2452 250 Petition to revive - unavoidable 1453 1,500 2453 750 Petition to revive - unintentional 1,400 1501 2501 700 Utility issue fee (or reissue) 1502 800 2502 400 Design issue fee 1503 1100 2503 550 Plant issue fee 1464 130 1460 130 Petitions to the Commissioner (CFR 1.17(h) Group III) 1463 200 Petitions to the Commissioner (CFR 1.17(g) Group II) 1460 200 1462 400 400 1460 Petitions to the Commissioner (CFR 1.17(f) Group I) 1807 50 1807 50 Processing fee under 37 CFR 1.17(q) 1806 180 1806 180 **Submission of Information Disclosure Stmt** 8021 Recording each patent assignment per 40 8021 40 property (times number of properties) 1809 790 2809 395 For filing a submission after final rejection (see 37 CFR 1.129(a)) 1814 130 2814 65 Statutory Disclaimer 1810 790 2810 395 For each additional invention to be examined (see 37 CFR 1.129(b)) 1801 790 2801 395 Request for Continued Examination (RCE) 1802 900 1802 900 Request for expedited examination of a design application Publication fee for early, voluntary, or normal pub. 1504 300 300 1504 1505 300 300 1505 Publication fee for republication 1803 130 1803 130 Request for voluntary publication or republication 1808 130 1808 130 Processing fee under 37 CFR 1.17(i) (except provisionals) 1454 1,370 1454 1,370 Acceptance of unintentionally delayed claim for priority Other fee (specify) Other fee (specify) SUBTOTAL (4) \$_ *Reduced by Basic Filing Fee Paid SUBMITTED BY: Typed or Printed∕Ñame<u>:</u> James H. Salter Signature: Date: Reg. Number: <u>35,668</u> Telephone Number: (408) 406-4855

Send to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Title: SOFTWARE TOOL FOR SYNTHESIZING A REAL-TIME OPERATING SYSTEM

<u>S/N 10/688,573</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert M. Zeidman

Examiner: Ben C. Wang

Serial No.:

10/688,573

Group Art Unit: 2196

Filed:

October 20, 2003

Docket No.: Zeid-01

Title:

SOFTWARE TOOL FOR SYNTHESIZING A REAL-TIME OPERATING

SYSTEM

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

This responds to the Office Action mailed on <u>November 21, 2006</u>. Please amend the above-identified patent application as follows.

IN THE CLAIMS

Please amend the claims as follows:

- 1) (Currently Amended) A method for developing a real-time operating system, comprising:
 - a) specifying a set of n tasks, task (1) through task(n), to be scheduled for execution, at least one of the tasks of said set of n tasks being selectively configurable as a preemptive or a non-preemptive task;
 - b) specifying an a scheduling algorithm for scheduling the execution of said set of n tasks; and
 - c) synthesizing source code <u>with embedded commands</u> to implement a task scheduler that uses said scheduling algorithm <u>and said embedded commands</u> for controlling execution of said set of n tasks.
- 2) (Currently Amended) The method of claim 1) further <u>including</u> specifying t init-tasks that are executed only once upon initial execution of said task scheduler, t being less than or equal to n.
- 3) (Currently Amended) The method of claim 1) further <u>including</u> specifying f f-loop tasks, each having an associated integer value l(i) for i ranging from 1 to f and f being less than or equal to n, said task scheduler including a continuously executing loop such that each f-loop task executes exactly once every l(i) times that the loop is executed.